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## Specification for Approval

Customer:_	_	
Model Nam	ie:	

Sı	Customer approval		
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		



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## **Revision Record**

REV NO.	REV DATE	CONTENTS	Note
Α	2024/11/21	NEW ISSUE	

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#### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

#### 2. General Information

#### LCM

ITEM	STANDARD VALUES	UNITS
LCD type	10.4"TFT	
Dot arrangement	1024×R.G.B.×768	dots
Color filter array	RGB vertical stripe	
Display mode	Normally Black	-
Viewing Direction	80/80/80/80	
Module size	262.25(W)×204.60(H)×10.70(T)	mm
Active area	210.432(W)×157.824(H)	mm
Dot pitch	0.2055(W)×0.2055(H)	mm
Interface	LVDS 6/8 bit	
Operating temperature	-30 ~ +80	°C
Storage temperature	-30 ~ +80	°C

#### **CTP**

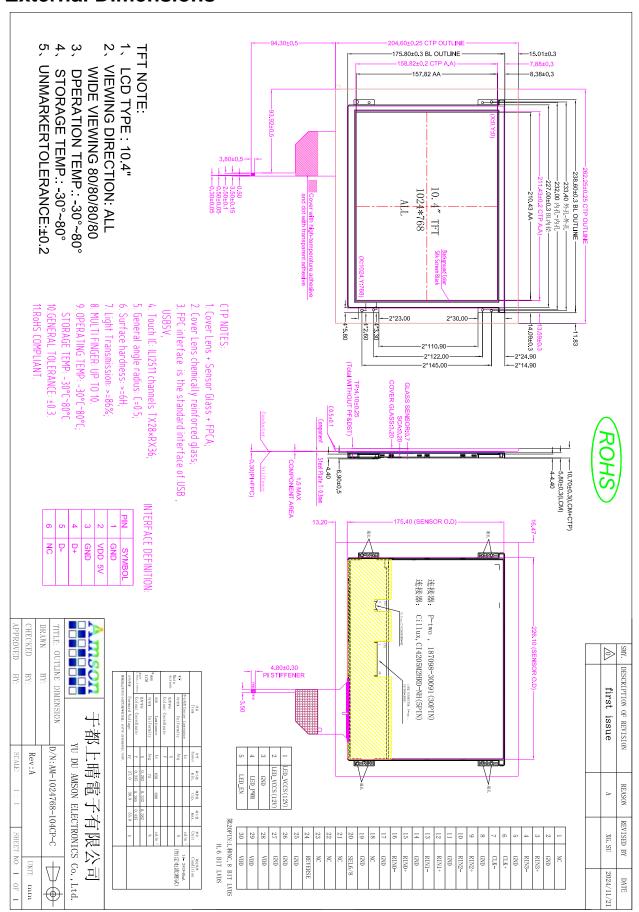
STANDARD VALUES	UNITS
Cover Lens + Sensor + FPC	
ILI2511	
≥85%	
6H	
262.25(W)×204.60(H)×4.10(T)	mm
211.43(W)×158.82(H)	mm
1024×768	
USB	
-30 ~ +80	°C
-30 ~ +80	°C
	Cover Lens + Sensor + FPC  ILI2511  ≥85%  6H  262.25(W)×204.60(H)×4.10(T)  211.43(W)×158.82(H)  1024×768  USB  -30 ~ +80



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#### 3. External Dimensions





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# 4. Interface Description 4.1TFT LCD MODULE CN1

CN1					
PIN	PIN NAME	DESCRIPTION	Remark		
1	NC	Reserved as BIST function for INX test	Note 1		
2	GND	Ground			
3	RIN 3+	Positive LVDS differential data input (+)			
4	RIN 3-	Negative LVDS differential data input (-)			
5	GND	Ground			
6	CLK+	Positive LVDS differential data input (+)			
7	CLK-	Negative LVDS differential data input (-)			
8	GND	Ground			
9	RIN 2+	Positive LVDS differential data input (+)			
10	RIN 2-	Negative LVDS differential data input (-)			
11	GND	Ground			
12	RIN 1+	Positive LVDS differential data input (+)			
13	RIN 1-	Negative LVDS differential data input (-)			
14	GND	Ground			
15	RIN0 +	Positive LVDS differential data input (+)			
16	RIN0 -	Negative LVDS differential data input (-)			
17	GND	Ground			
18	NC	No connection			
19	GND	Ground			
20 SEL 6/8		Selection for 6 bits/8bit LVDS data input Low or NC : 8 bit input mode High : 6 bit input mode	Note 2		
21	21 NC Reversed as EE_WP for OTP function		Note 3		
22	NC	Reversed as EE_SDA for OTP function	Note 3		
23	NC	Reversed as EE_SCL for OTP function	Note 3		
24	REVERSE	Reverse panel function (Display rotation)	Note 4		
25-27	GND	Ground			
28-30	VDD	Power supply: + 3.3V			
_		DC CONN 20mins D two 407000 20004			

## Connector:Input LVDS CONN,30pins, P-two, 187098-30091 Note:

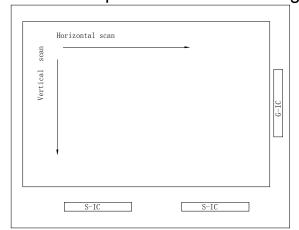
- 1. Pin1 is reversed as BIST function for test, don't connect signal to this pin, keep floating.
- 2. SEL6/8 is used for selecting 6bit/8bit LVDS data input, L or NC: 8bit; High:6bit.
- 3. Pin21,22,23 are used as SPI interface for OTP function, don't connect any signal to these pin, and don't short them, keep floating.



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4. Reverse pin is used for selecting scanning direction.



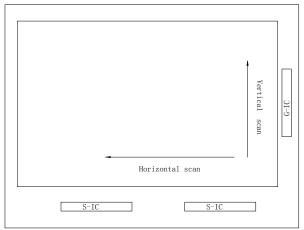


Fig. 1 Normal scan (Pin24, Reverse = Low or NC)

Fig. 2 Reverse scan (Pin24, Reverse = High)

## 4.2 BACKLIGHT CONVERTER (Converter connector pin) CN2

PIN	PIN NAME	DESCRIPTION			
1	LED_VCCS	12V input			
2	LED_VCCS	12V input			
3	GND	Ground			
4	LED_PWM	PWM			
5	LED_EN	Converter power IC Enable (Active High)			

## Connector: Input BL power CONN, 5pins, Cillux,CI4205M2HRD-NH 4.3 BACKLIGHT LED PIN

#### CN<sub>3</sub>

•	9110			
	PIN	PIN NAME	DESCRIPTION	
	1	LED+	Red wire	
	2	LED1-	White wire	
	3	LED2-	White wire	

## Connector: Output BL power CONN, 3pins, Cillux, CI4203M2HRD-NH 4.4 CTP Description

Pin	Pin Name	Description	Remark
1	GND	Ground	
2	VDD	VDD 5V	
3	GND	Ground	
4	D+	DATE+	
5	D-	DATE-	
6	NC	NC	



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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Digital Supply Voltage	VDD	-0.3	4.0	V	
VIN Voltage	VLED	-0.3	50	V	
Operating Temperature	Тор	-30	80	°C	
Storage Temperature	Тѕт	-30	80	°C	

### 6. DC Characteristics

#### **6.1 Parameter**

Item	Cumbal	Values			Unit	Remark
item	Symbol	Min.	Тур.	Max.	Unit	кетагк
Dower voltage	VDD	3.0	3.3	3.6	>	
Power voltage	LED_VCCS	11	12	13	>	
Input logic high voltage	V <sub>IH</sub>	0.7VDD	-	VDD	٧	1
Input logic low voltage	$V_{IL}$	0	-	0.3VDD	>	
	I <sub>VDD</sub>		385	424	mA	VDD =3.3V@frame 60 Hz, White pattern
Current for Power	I <sub>LED_VCCS</sub>	-	0.52	-	A	100% PWM Duty @ VLED+ =33V, ILED=80mA*2
LED_EN Control Level	BL On	3.0	-	5	٧	
LED_EN CONTO Level	BL Off	0	-	0.3	٧	
LED_PWM Control Level	PWM High Level	3.0	-	5	>	
	PWM Low Level	0	-	0.3	٧	
LED_PWM Control Frequency	f <sub>PWM</sub>	1K	-	20K	Hz	2



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Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Digital Supply Voltage	VDD	2.75	3.3	3.6	٧	
VIN Voltage	LED_VCCS	-0.3	-	25	V	
Input logic high voltage	VIH	0.7*VDD	-	VDD	V	
Input logic low voltage	VIL	GND	-	0.3*VDD	V	

Note 1: Including signal: SEL6/8 & Reverse

Note 2: LED\_PWM duty >10%.

6.2 BL power output

5.2 22 pono. Garpar							
			Values				
Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
Voltage for LED backlight	V <sub>led</sub>	27	30	33	V	1	
Current for LED backlight	I <sub>led</sub>		160		mA	2	

Note 1: output power LED+ OVP is 40V.

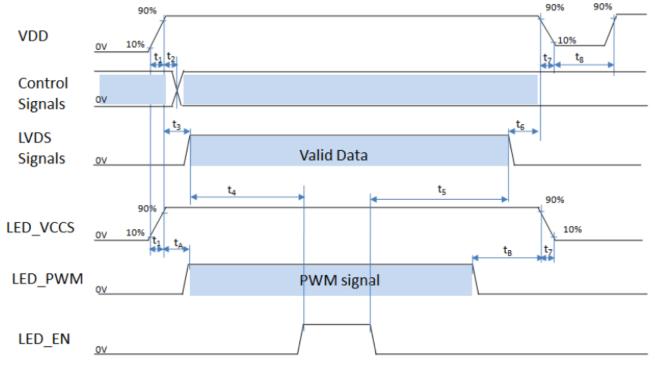
Note 2: Set BL feedback 2 channels, each channel feedback current is 80mA

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#### 6.3 Power sequence

The power sequence specifications are shown as the following table and diagram.

O: mah al	Va	l lmi4	
Symbol	Min.	Max.	Unit
t <sub>1</sub>	1	20	ms
t <sub>2</sub>	1	5	ms
<b>t</b> <sub>3</sub>	10	50	ms
t <sub>4</sub>	200	500	ms
<b>t</b> <sub>5</sub>	200	500	ms
<b>t</b> <sub>6</sub>	50	200	ms
t <sub>7</sub>	0	20	ms
t <sub>8</sub>	500	-	ms
<b>t</b> A	0	50	ms
t <sub>B</sub>	0	50	ms



- Note 1: Please don't plug the interface cable of on when system is turned on.
- Note 2: Please avoid floating state of the interface signal during signal invalid period.
- Note 3: It is recommended that the backlight power must be turned on after the power supply for LCD and the interface signal is valid.
- Note 4: Control signals include SEL6/8 & Reverse.



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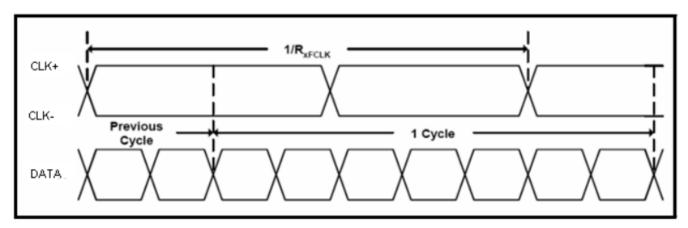
#### 7. LVDS SIGNAL TIMING CHARACTERISTICS

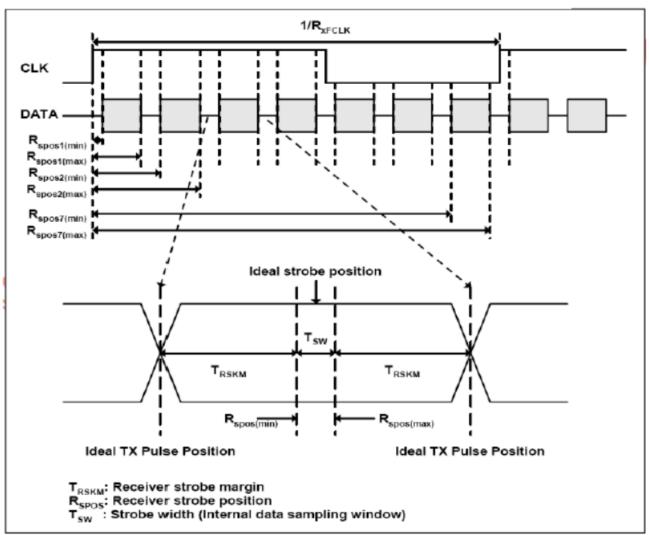
#### 7.1AC Electrical characteristics

Parameter	Symbol	Min	Тур	Max	Unit s	Condition
Clock frequency	RxFCLK	26.2	51.2	71	MHz	
Input data skew margin	TRSKM	500	500	1/(2*RxFCLK)	ps	Typical value for 1024*600 resolution
Clock high time	TLVCH		4/(7xRxFCLK)		ns	VID =400mv RxVCM=1.2V RxFCLK=71MHz VDD_LVDS=3.3V
Clock low time	TLVCL		3/(7xRxFCLK)		ns	
VSD setup time	TenPLL	0	TenPLL	150	us	

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#### 7.2 Input clock and data timing diagram



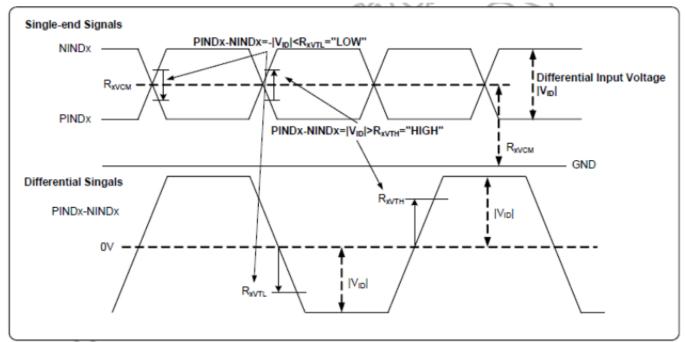


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#### 7.3 DC electrical characteristics

Bouwater	O. mah al		Values	I I mit	Damank		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark	
LVDS Differential input high Threshold voltage	R <sub>xVTH</sub>	-	-	+100	mV	D -1.2V	
LVDS Differential input low Threshold voltage	R <sub>xVTL</sub>	-100	1	-	mV	R <sub>XVCM</sub> =1.2V	
Input Voltage range (Singled-end)	R <sub>xVIN</sub>	0	1	VDD-1.2+  V <sub>ID</sub>  /2	V		
LVDS Differential input common mode voltage	R <sub>xVCM</sub>	V <sub>ID</sub>  /2	-	VDD-1.2	V		
LVDS Differential voltage	V <sub>ID</sub>	0.2	-	0.6	V		



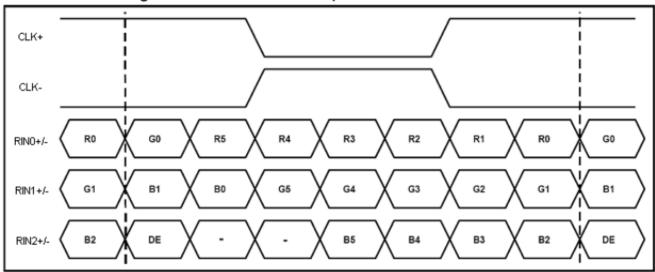
#### 7.4 data timing

Parameter	Symbol		Unit		
Farameter	Symbol	Min.	Тур.	Max.	Onic
DCLK frequency	fclk	52	65	71	MHz
Horizontal display area	thd		1024		DCLK
HSD period	th	1114	1344	1400	DCLK
HSD blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd		768		T <sub>H</sub>
VSD period	tv	778	806	845	T <sub>H</sub>
VSD blanking	tvbp+tvfp	10	38 🛆	(//17)	T <sub>H</sub>

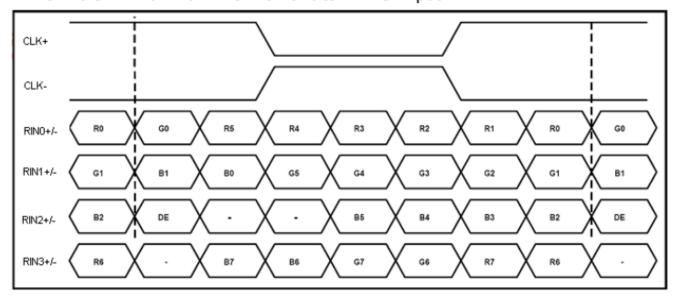
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#### 7.5 LVDS data input format

SEL6/8 = "High" for 6 bits LVDS Input



SEL6/8 = "Low" or "NC" for 8 bits LVDS Input





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### 8. Backlight Characteristic

Item		Symbol	MIN	TYP	MAX	UNIT	NOTE
Backlight Pow	er	LED_VCCS	-0.3	12	25	V	Ta = 25°C
Backlight Pow	er	ILED_VCCS	ı	0.5	0.8	А	LED_VCCS=12V
EN Signal Volta	VIH	LED EN	3.0		5.0	V	
ge	VIL	LED_EN	GND		0.3	V	
Luminous Intens	VIH	LED_PWM	3.0		5.0	V	
ity for LCM	VIL	LED_F VVIVI	GND		0.3	V	
PWM Frequen	су	LED_PWM	1K	ı	20K	Hz	
Lifetime			50000	ı	-	Hr	
Color	White						
Average Brightness		-	650	800	-	Cd/cm2	
Luminance unifo	rmity	-	75	-	_	%	

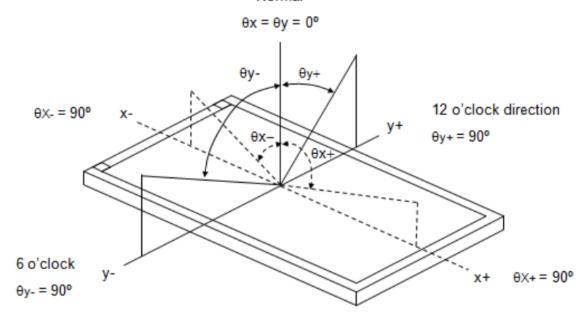
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#### 9. Optical Characteristics

Item	Condition	S	Min.	Тур.	Max.	Unit	Note	
	Horizontal	θL	80	-	-			
Viewing Angle	Tionzontai	θR	80	-	-	dograa	(1)	
(CR>10)	Vertical	θт	80	-	-	degree	(1)	
	vertical	θв	80	-	-			
Contrast Ratio	Center		700	1000	-	-	(2)	
Doonongo Timo	Rising			14	19	ms	(2)	
Response Time	Falling		<b>-</b>	11	16	ms	(3)	
	Red x			0.651		-	(4)	
	Red y			0.345		-		
	Green x			0.315		-		
CF Color	Green y			0.611		-		
Chromaticity (CIE1931)	Blue x		TYP.	0.145	TYP.	-		
	Blue y		-0.05	0.093	+0.05	-		
	White x			0.326		-		
	White y			0.383		-		

Note (1)Definition of Viewing Angle ( $\theta x$ ,  $\theta y$ ): Viewing angles are measured by BM5A

#### Normal



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#### Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

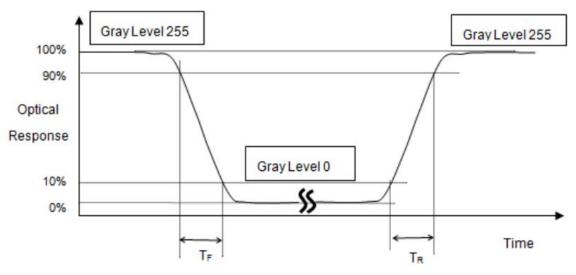
Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

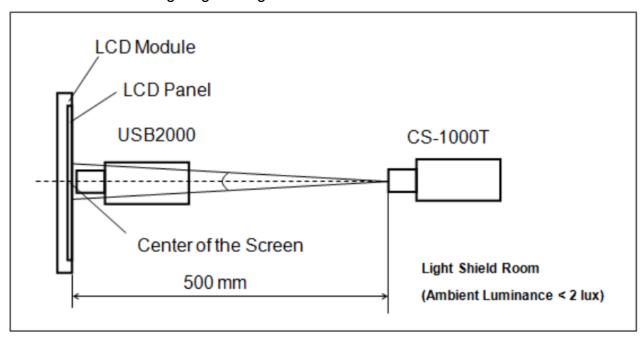
CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in

Note (3) Definition of Response Time (TR, TF):



#### Note (4)Measurement Setup:

The LCD assembly should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.





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10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION					
1	High Temperature Storage	Keep in 80°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
2	Low Temperature Storage	Keep in -30°C ±5°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
3	High Temperature Operating Test	80°C*96Hrs					
4	Low Temperature Operating Test	-30℃*96Hrs					
(5)	High Temperature / High Humidity Operating Test	60 ℃ / 90% R.H ,96 hrs.					
6	High Temperature / High Humidity Storage Test	Keep in 60 °C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
7	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					
		Air Discharge: Apply 6 KV with 5 times Discharge for each polarity +/-  Contact Discharge: Apply 250 V with 5 times discharge for each polarity +					
8	ESD Test	<ol> <li>Temperature ambiance: 15°C~35°C</li> <li>Humidity relative: 30%~60%</li> <li>Energy Storage Capacitance( Cs + Cd ): 150pF±10%</li> <li>Discharge Resistance(Rd): 330Ω±10%</li> <li>Discharge, mode of operation:</li> <li>Single Discharge (time between successive discharges at least 1 sec)</li> <li>(Tolerance if the output voltage indication: ±5%)</li> </ol>					
9	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X、Y、Z) duration for 2 Hrs</li> </ol>					
(10)	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm)  0 ~ 45 4 122  45.4 ~ 90.8 76  90.8 ~ 454 61  Over 454 46  Drop  Direction: **1 corner / 3 edges / 6 sides each 1time					



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#### 11. Inspection Standard

#### 11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

#### 11.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

#### 11.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL: II

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

#### 11.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

#### 11.2. CHECKING CONDITION

**11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.

**11.2.2.**CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE **Ambient Illumination:** 

Functional detection in 600nits backlight environment Appearance detection in 800~1000 Lux external environment



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#### 11.3. INSPECTION PLAN:

TI.S. INST EU	TION I LAIN.		
CLASS	ITEM	JUDGEMENT	CLASS
	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO.", "LOT NO." AND "QUANTITY"	Minor
PACKING &		SHOULD INDICATE ON THE PACKAGE.	
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED	Critical
		QUANTITY SHORT OR OVERREJECTED	
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON	Major
		THE PRODUCT	
	4. DIMENSION,	ACCORDING TO SPECIFICATION OR	
ASSEMBLY	LCD GLASS SCRATCH	DRAWING.	Major
	AND SCRIBE DEFECT.		
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE	Minor
		IS VISABLE IN THE VIEWING AREA	
		REJECTED	
	6. BLEMISH - BLACK SPOT -	ACCORDING TO STANDARD OF VISUAL	Minor
	WHITE SPOT IN THE LCD	INSPECTION(INSIDE VIEWING AREA)	
	AND LCD GLASS CRACKS		
	7. BLEMISH - BLACK SPOT	ACCORDING TO STANDARD OF VISUAL	Minor
APPEARANCE	WHITE SPOT AND SCRATCH	INSPECTION(INSIDE VIEWING AREA)	
	ON THE POLARIZER		
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL	Minor
		INSPECTION(INSIDE VIEWING AREA)	
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR ( OR NEWTON	
		RING) OF LCDREJECTED.	Minor
		OR ACCORDING TO LIMITED SAMPLE	
		( IF NEEDED, AND INSIDE VIEWING AREA )	
	10. ELECTRICAL AND OPTICAL	ACCORDING TO SPECIFICATION OR	Critical
	CHARACTERISTICS	DRAWING . ( INSIDE VIEWING AREA )	
	( CONTRAST: VOP:		
	CHROMATICITY ETC )		
ELECTRICAL	11.MISSING LINE	MISSING DOT, LINE, CHARACTER	Critical
		REJECTED	
	12.SHORT CIRCUIT	NO DISPLAY - WRONG PATTERN	Critical
	WRONG PATTERN DISPLAY	DISPLAY · CURRENT CONSUMPTION	
		OUT OF SPECIFICATION REJECTED	
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL	Minor
		INSPECTION	



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NO.	CLASS	ITEM		JUD	GEMENT	
			(A) ROUN	ID TYPE:	unit: mm	
			DIA	METER (mm.)	ACCEPTABLE Q'TY	
				Ø≤ <b>0</b> .15	Disregard(interval≥1mm)	
		BLACK AND WHITE	0.15	< ∅ ≤ 0.4	3 (Distance ≥ 15mm)	
		SPOT FOREIGN	0.4	< Ø	0	
11.4.1	MINOR	MATERIEL DUST IN	NOTE	E: ∅=(LENGTH*WII	DTH)/2	
		THE CELL BLEMISH	(S) LINEA	R TYPE:	unit: mm	
		SCRATCH	LENGTH	WIDTH	ACCEPTABLE QTY	
				W≤ 0.03	B Disregard≥ 1mm	
			L ≤5.0	$0.03 < W \le 0.07$	3 (Distanced ≥ 15mm	
				0.07 < W	FOLLOW ROUND TYPE	
			NOTE: Ø	=(LENGTH*WIDTH	)/2	
					unit: mm.	
				DIAMETER	ACCEPTABLE Q'TY	
		BUBBLE IN POLARIZER		Ø<0.2	Disregard(interval≥1mm)	
11.4.2	MINOR	DENT ON POLARIZER	C	0.2<∅≤ 0.5	3 (Distance≥ 15mm)	
			C	0.5<∅	0	
				Items	ACC. Q'TY	
				Bright dot	N ≤ 2(Distance ≥ 15mm)	
			Dark dot		N ≤ 3(Distance ≥ 15mm)	
11.4.3	MINOR		Pixel Defi	Pixel  Dot Dot Dot Dot		
11.4.3 MINOR		R Dot Defect	<ol> <li>Note:         <ol> <li>The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.                 Definition:&lt;1/2 dot and visible by 5% ND filter</li> </ol> </li> <li>Bright dot: Dots appear bright and unchanged in size which LCD panel is displaying under black pattern.</li> <li>Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure Red, Green, Blue pattern.</li> </ol>			
11.4.3.1	MINOR	Mura	Not visible through 5% ND filter in 50% gray or judge by lim sample if necessary			



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NO.	CLASS	ITEM	JUDGEMENT
11.4.5	MINOR	LCD GLASS CHIPPING	X ≥ 3mm Y > S Reject
11.4.6	MINOR	LCD GLASS CHIPPING	X or Y > S Reject
11.4.7	MAJOR	LCD GLASS GLASS CRACK	Continuous burst NG Reject
11.4.8	MAJOR	LCD GLASS SCRIBE DEFECT	ACCORDING TO DIMENSION
11.4.9	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL AREA )	Y<1/2Z $Y \ge 0.5 \text{mm}_{\text{Reject}}$ $X \ge 3 \text{mm}$
11.4.10	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL SURFACE )	$Y<1/2Z$ $Y \ge 0.5 mm$ $X \ge 3 mm$
11.4.11	MINOR	LCD GLASS CHIPPING	$X \geqslant 3mm$ $Y \geqslant T$ Reject $Z$ If touch the electrode lines, the need to retain the two-thirds electrode lines



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#### 12. Handling Precautions

#### 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.



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#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 14. Packing Method TBD