



# Specification for Approval

Customer: \_\_\_\_\_

Model Name: \_\_\_\_\_

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
<i>Peter</i>	<i>Peng Jun</i>		



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## 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

## 2. General Information

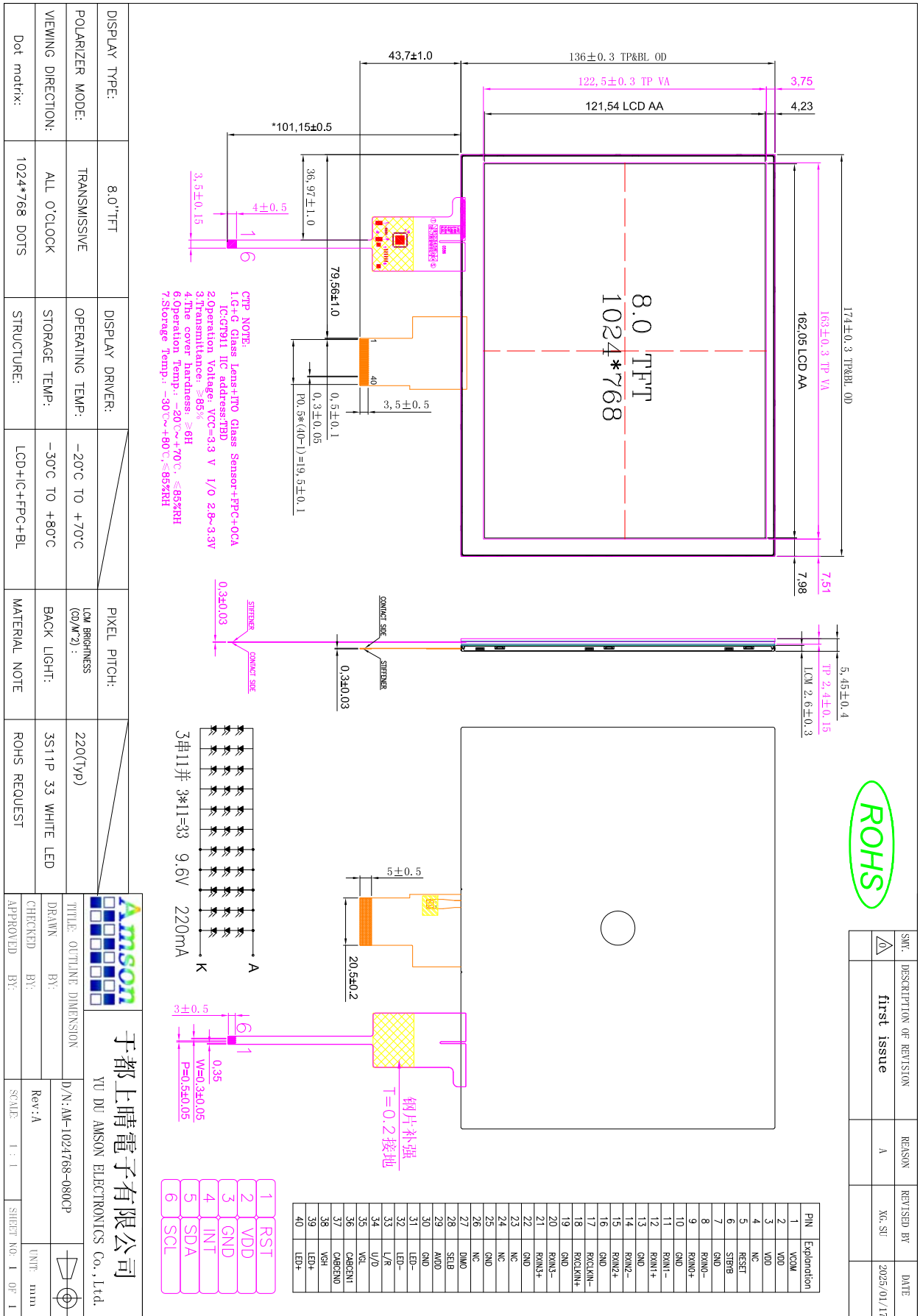
### LCM

TITEM	STANDARD VALUES	UNITS
LCD type	8.0" TFT	--
Dot arrangement	1024×RGB×768	dots
Color filter array	RGB vertical stripe	--
Display mode	Normally black	-
Gray Scale Inversion Direction	85/85/85/85	--
Eyes Viewing Direction	ALL	
Module size	174.00(W)×136.00(H)×5.45(T)	mm
Active area	162.05(W)×121.54(H)	mm
Dot pitch	0.15825(W)×0.15825(H)	mm
Interface	LVDS	--

### CTP

ITEM	STANDARD VALUES	UNITS
CTP type	Cover Lens + sensor + FPC	--
CTP Driver IC	GT911	--
Transmittance	≥85%	--
The cover hardness	6H	--
CTP size	174.0(W)×136.0(H)×2.4(T)	mm
CTP Viewing area	163.0(W)×122.5(H)	mm
CTP Interface	I2C	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C

## 3. External Dimensions



## 4. Interface Description

PIN NO.	Symbol	I/O	Description
1	VCOM	P	Common Voltage
2	VDD	P	Power Voltage for digital circuit
3	VDD		
4	NC	P	No connection
5	RESET	I	Global reset pin
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	P	Power ground
8	RXIN0-	I	- LVDS differential data input
9	RXIN0+	I	+ LVDS differential data input
10	GND	P	Power ground
11	RXIN1-	I	- LVDS differential data input
12	RXIN1+	I	+ LVDS differential data input
13	GND	P	Power ground
14	RXIN2-	I	- LVDS differential data input
15	RXIN2+	I	+ LVDS differential data input
16	GND	P	Power ground
17	RXCLKN-	I	- LVDS differential clock input
18	RXCLKN+	I	+ LVDS differential clock input
19	GND	P	Power ground
20	RXIN3-	I	- LVDS differential data input
21	RXIN3+	I	+ LVDS differential data input
22	GND	P	Power ground
23,24	NC	-	No connection
25	GND	P	Power ground
26	NC	-	No connection
27	DIMO	O	Backlight CABC controller signal output
28	SELB	I	6bit / 8bit mode select
29	AVDD	P	Power for Analog Circuit
30	GND	P	Power ground
31,32	LED-	P	LED Cathode
33	L/R	I	Horizontal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Gate OFF Voltage
36	CABCEN1	I	CABC H/W enable
37	CABCEN0	I	CABC H/W enable
38	VGH	P	Gate ON Voltage
39,40	LED+	P	LED Anode

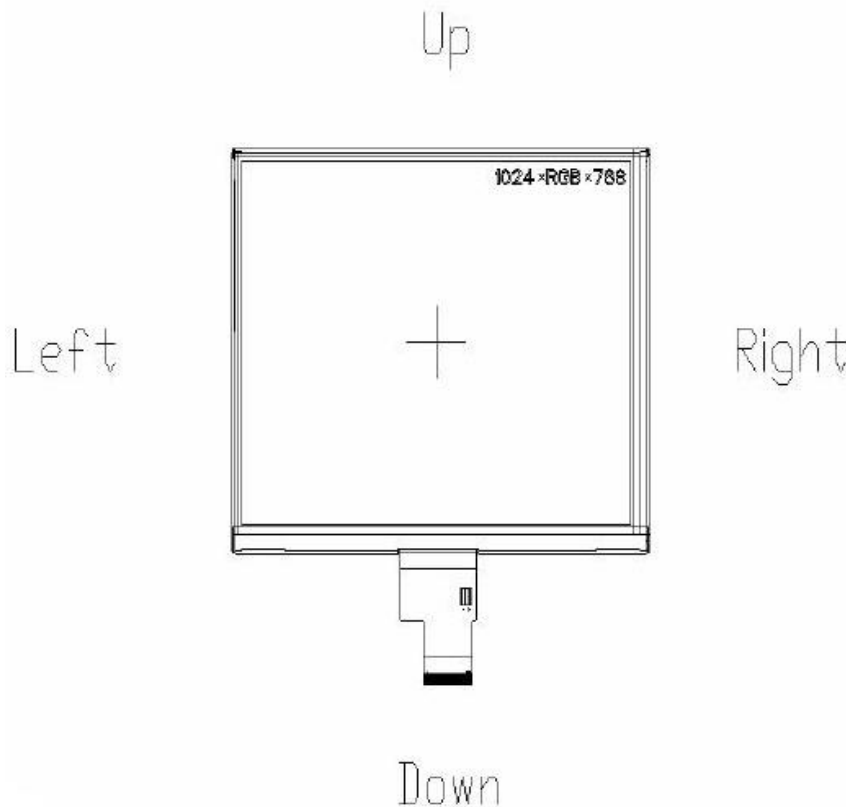
I: input, O: output, P: Power

Note1: If LVDS input data is 6 bits ,SELB must be set to High;  
If LVDS input data is 8 bits ,SELB must be set to Low.

Note2: When CABC\_EN="00", CABC OFF.  
When CABC\_EN="01", user interface image.  
When CABC\_EN="10", still picture.  
When CABC\_EN="11", moving image.  
When CABC off, don't connect DIMO, else connect it to backlight.

Note3: When L/R="0", set right to left scan direction.  
When L/R="1", set left to right scan direction.  
When U/D="0", set top to bottom scan direction.  
When U/D="1", set bottom to top scan direction.

Note: Definition of scanning direction.  
Refer to the figure as below:



### CTP

PIN NO.	Symbol	Description
1	INT	CTP interruption signal.
2	RES	CTP reset pin. Active low to enter reset state.
3	SDA	CTP I2C_data.
4	SCL	CTP I2C_clock.
5	TPVDD	Power supply.
6	TPGND	Power ground

## 5. Absolute Maximum Ratings

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table

Item	Symbol	Min.	Max.	Unit
Digital Supply Voltage	VDD	-0.3	+5.0	V
	AVDD	6.5	13.5	V
	VGH	-0.3	40	V
	VGL	-20	+0.3	V
Operating temperature	T <sub>OP</sub>	-20	70	°C
Storage Temperature	T <sub>STG</sub>	-30	80	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

Note 3: These range above is maximum value not the actual operating temperature . Actual Operating temperature is no more than 40°C and temperature refers to the LCM surface temperature;

Note 4: GWD is not responsible for product problems beyond the use conditions.



## 6. DC Characteristics

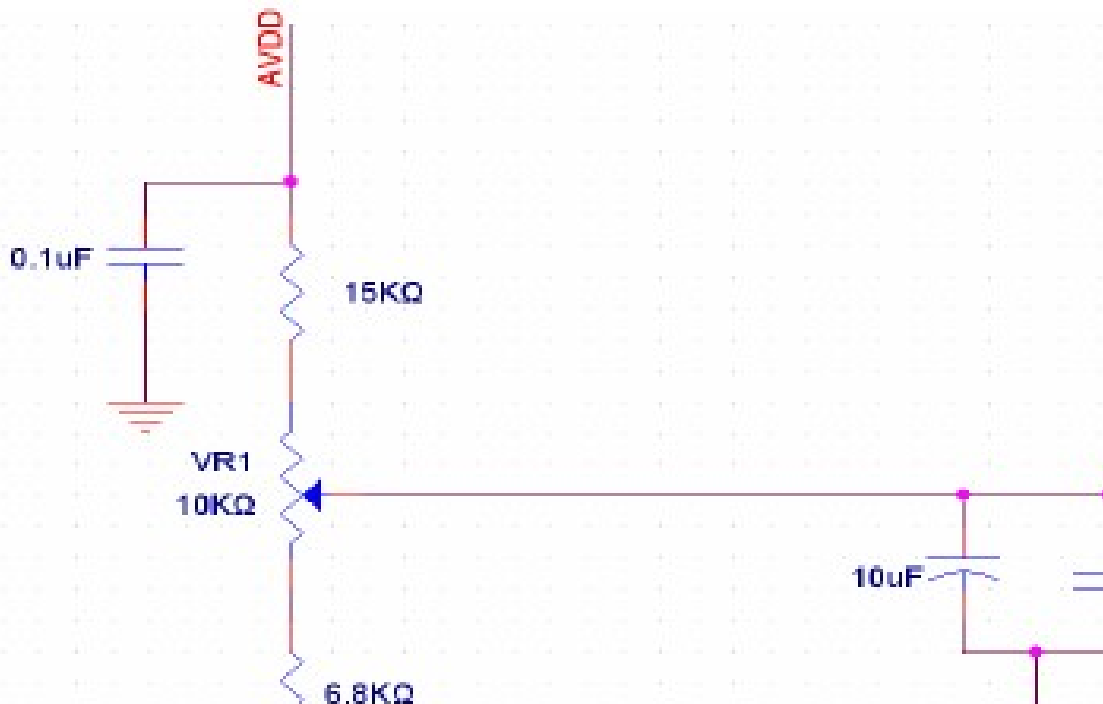
Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage		VDD	3.0	3.3	3.6	V
		AVDD	9.8	10	10.2	
		VGH	18.6	18.9	19.2	V
		VGL	-8.1	-7.8	-7.5	V
Input signal voltage		VCOM	2.6	3.6	4.6	V
Current Consumption All Black	Logic	I <sub>CC</sub> + I <sub>IN</sub>	-	TBD	-	mA
	Analog					

Note 1: Be sure to apply VDD and VGL to the LCD first, and then apply VGH.

Note 2: VDD setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: Typical Vcom is only a reference value, it must be optimized according to each LCM, please use VR and base on below application circuit..

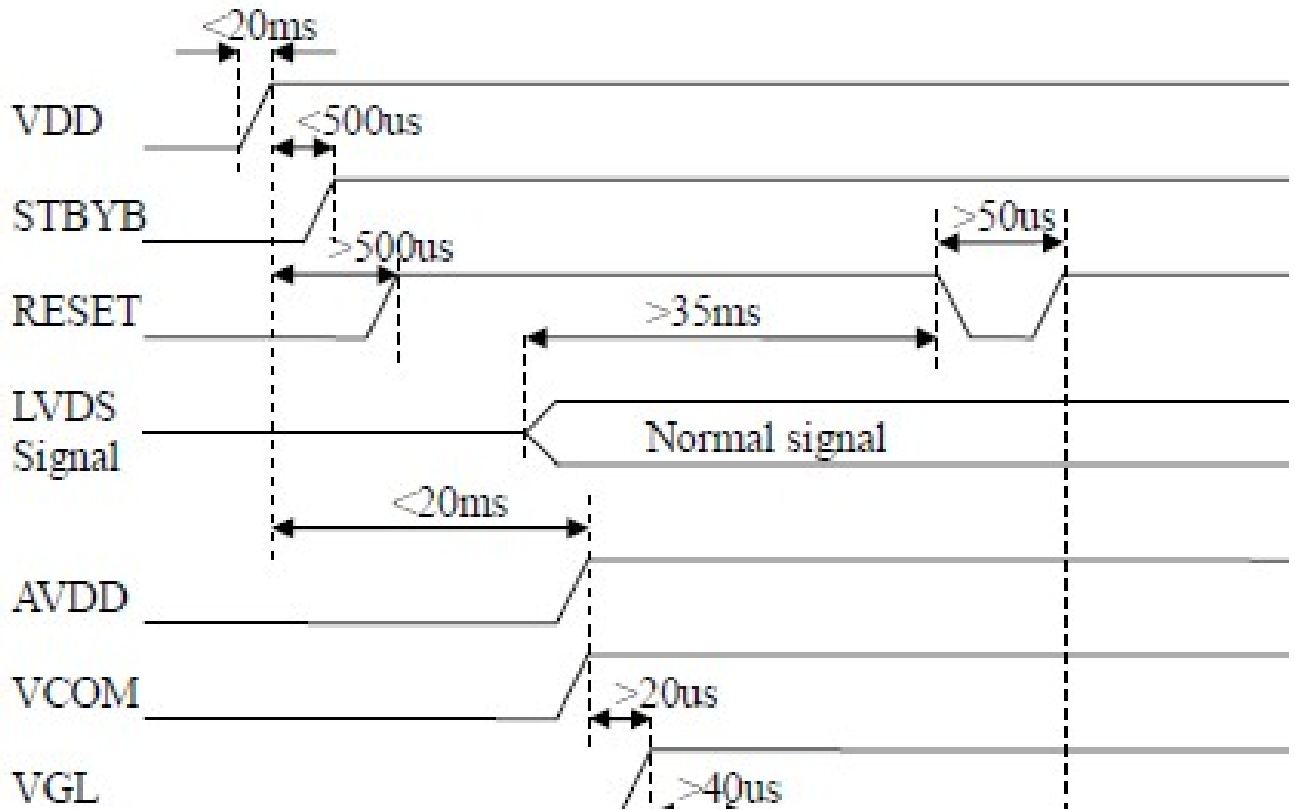
Note 4: RESET, STBYB, SELB, L/R, U/D, CABCE0, CABCE1.



7. Timing Characteristics

7.1 Power Sequence

a. Power on:

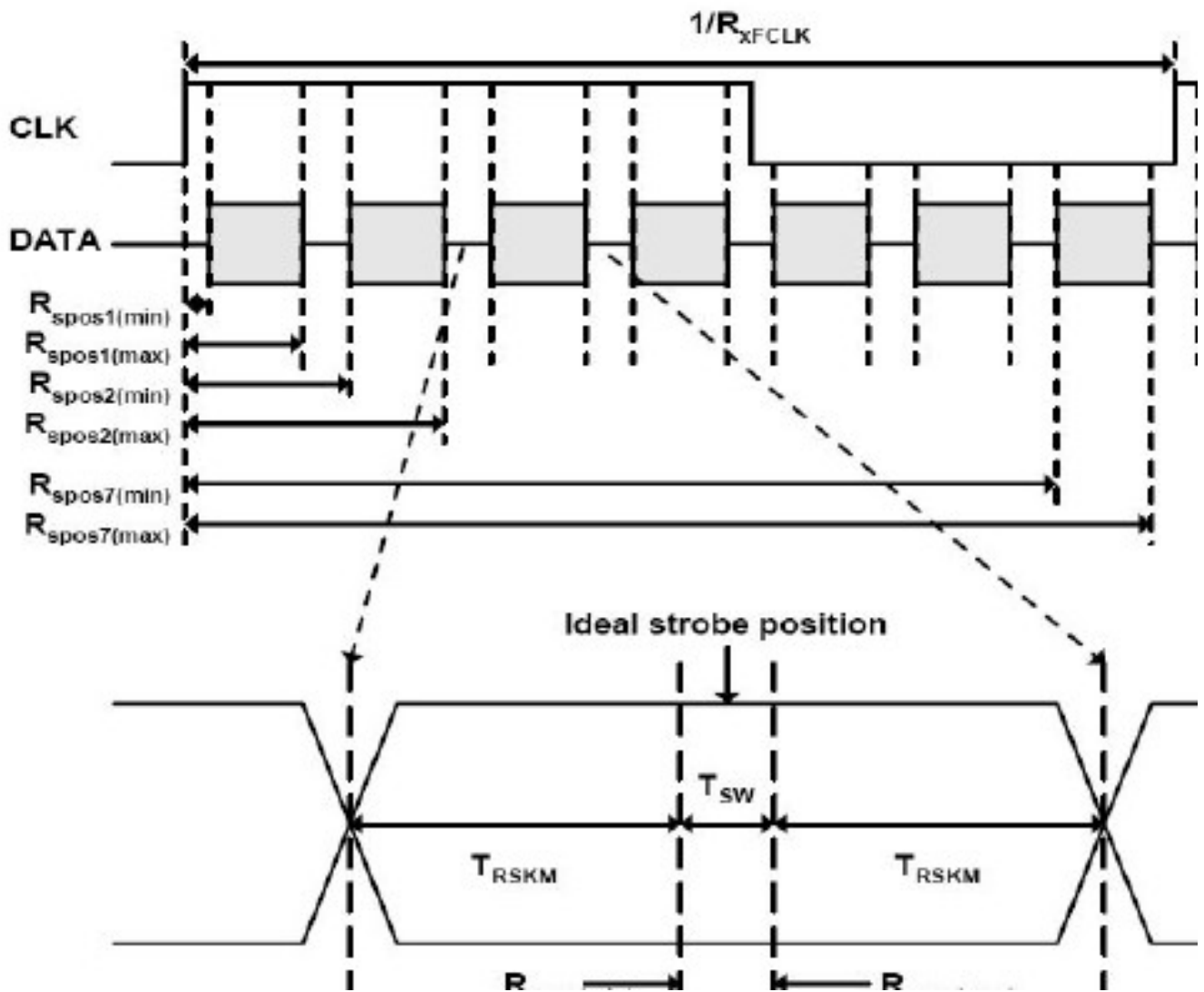


## 7.2 LVDS Signal Timing Characteristics

### AC Electrical Characteristics

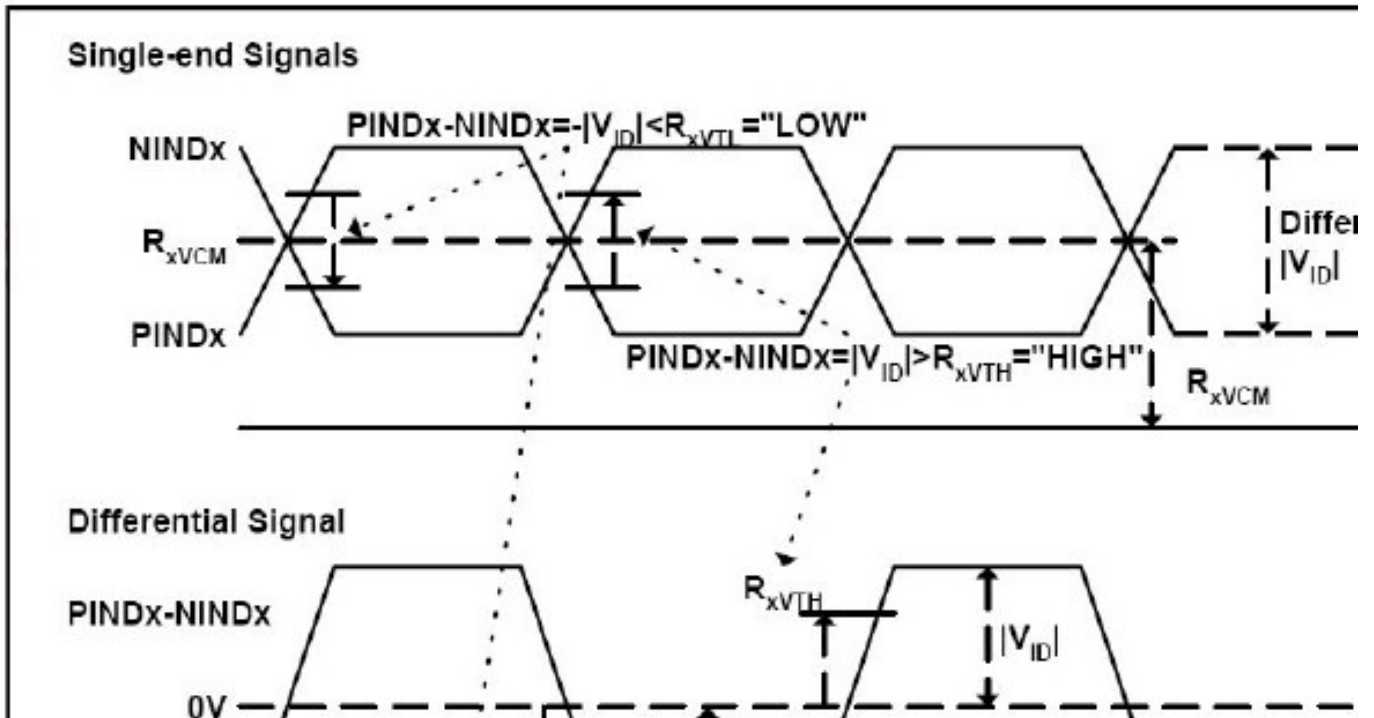
Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Clock frequency	$R_{xFCLK}$	20	-	71	MHz
Input data skew margin	$T_{RSKM}$	500	-	-	ps
Clock high time	$T_{LVCH}$	-	$4/(7 * R_{xFCLK})$	-	ns

### Input Clock and Data Timing Diagram



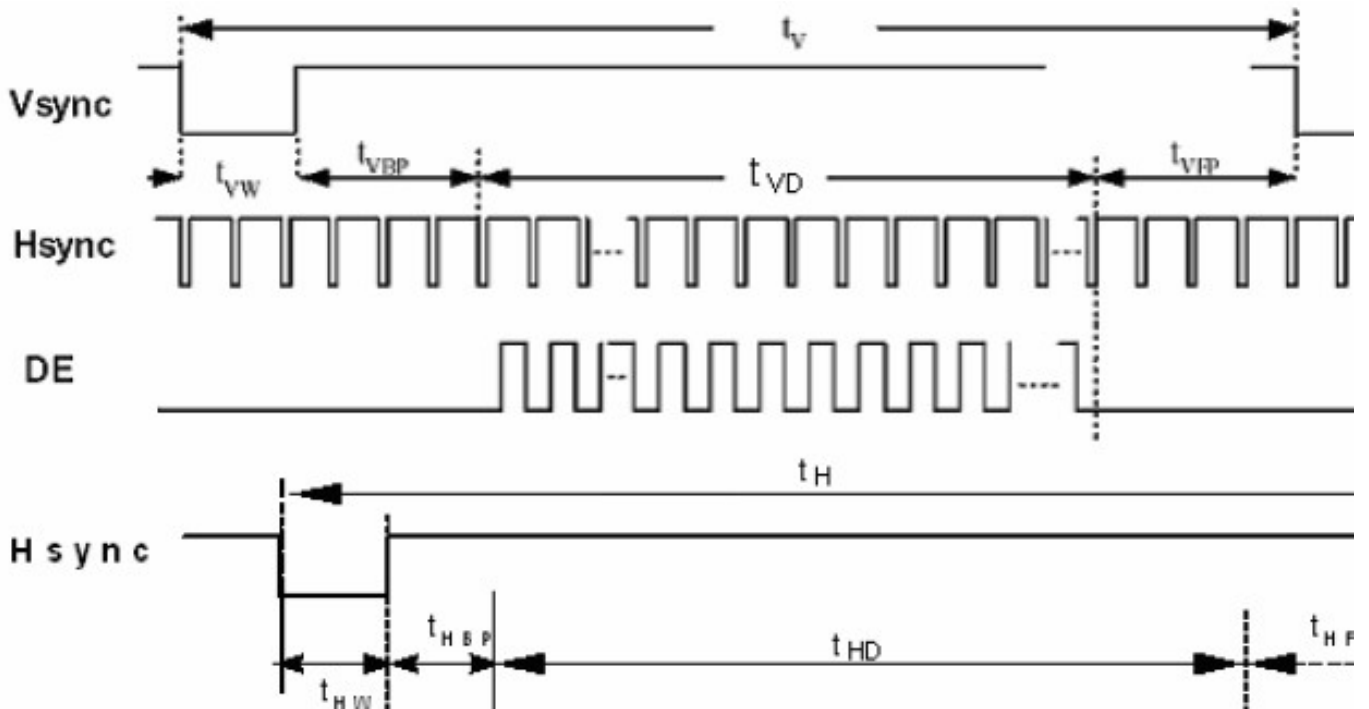
## 7.3 DC Electrical Characteristics

Parameter	Symbol	Values		
		Min.	Typ.	Max.
Differential input high Threshold voltage	$R_{xVTH}$	-	-	+0.1
Differential input low Threshold voltage	$R_{xVTL}$	-0.1	-	-
Input voltage range (singled-end)	$R_{xVIN}$	0	-	2.4
Differential input common mode voltage	$R_{xVCM}$	$ V_{ID} /2$	-	$2.4 -  V_{ID} /2$



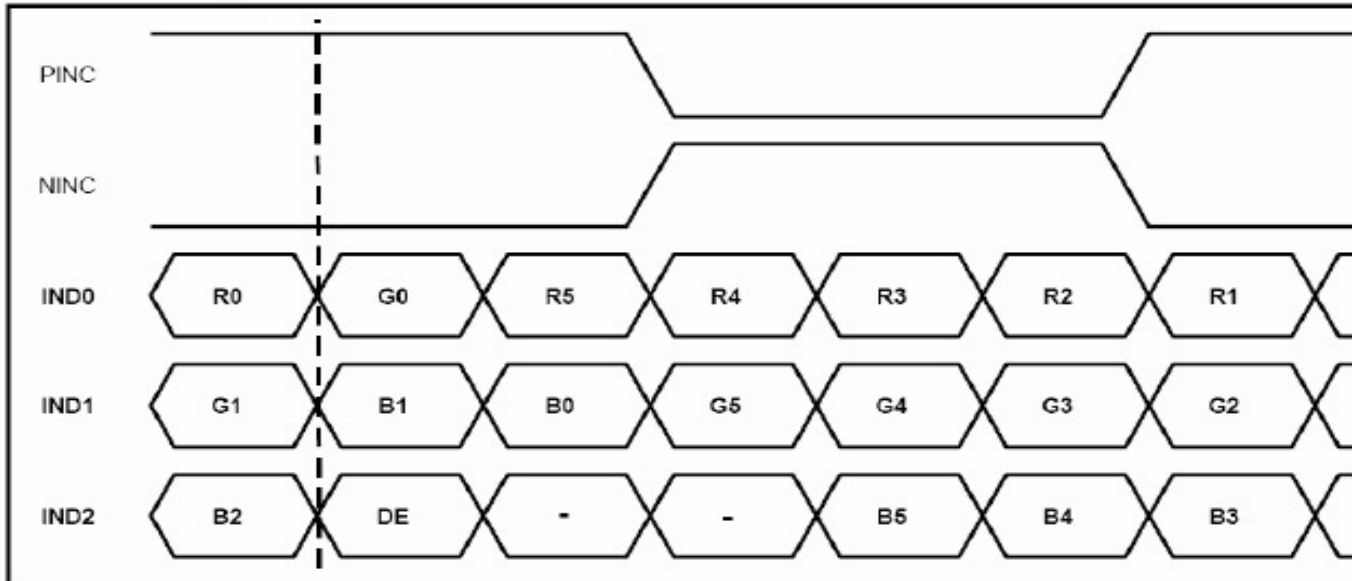
## 7.4 Timing Table

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Clock Frequency	fclk	52	65	71	MHz
Horizontal display area	thd	1024			
HS period time	th	1114	1344	1400	DCLK
HS Blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	768			

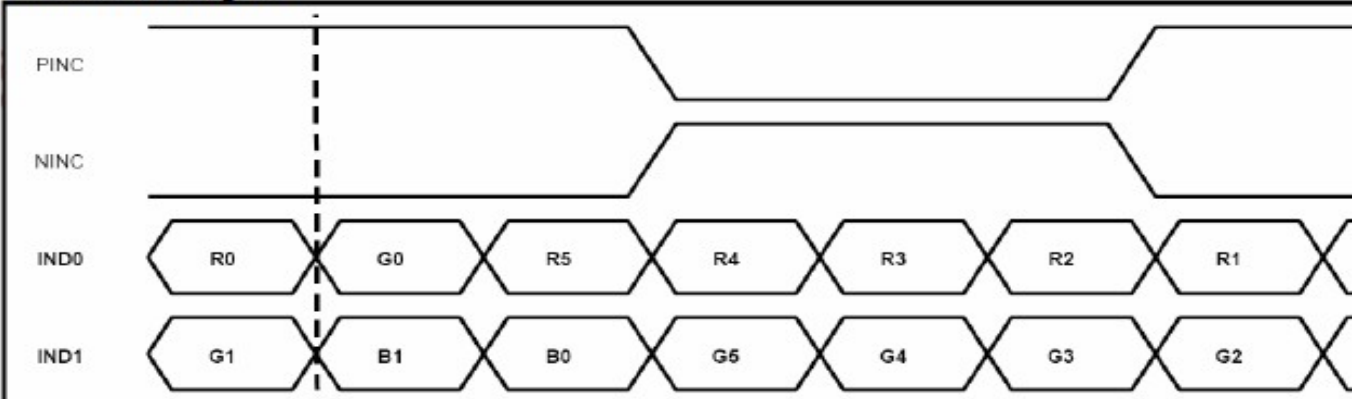


### 7.5 Data Input Format

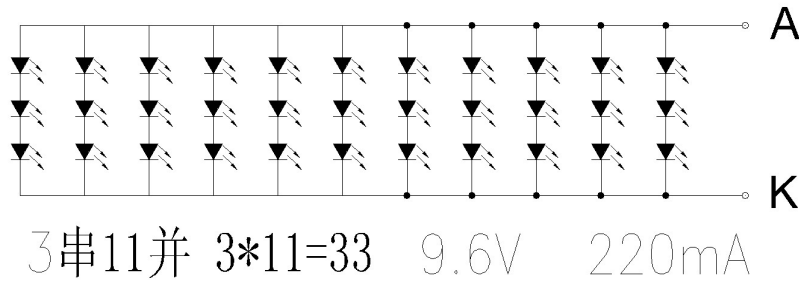
#### 6bit LVDS input



#### 8bit LVDS input



## 8. Backlight Characteristic



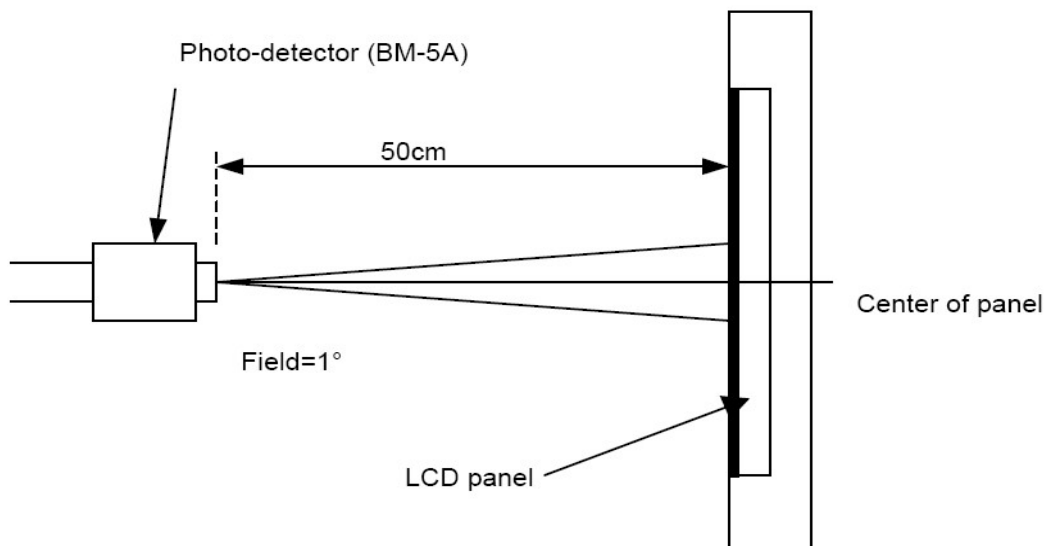
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	VF	Ta=25 °C, IF=40mA/LED	8.4	9.6	10.4	V
Forward Current	IF	Ta=25 °C, VF=3.2V/LED	-	220	-	mA
Power dissipation	PD	-	-	2112	-	mW
Uniformity	Avg	-	-	80	-	%
LED working life(25°C)	-	-	-	30,000	-	Hrs
Drive method	Constant current					
LED Configuration	30 White LEDs ( 3 LEDs in one string and 10 groups in parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness. The environmental conducted under ambient air flow, at Ta=25±2 °C,60%RH±5%, IF=40mA

## 9. Optical Characteristics

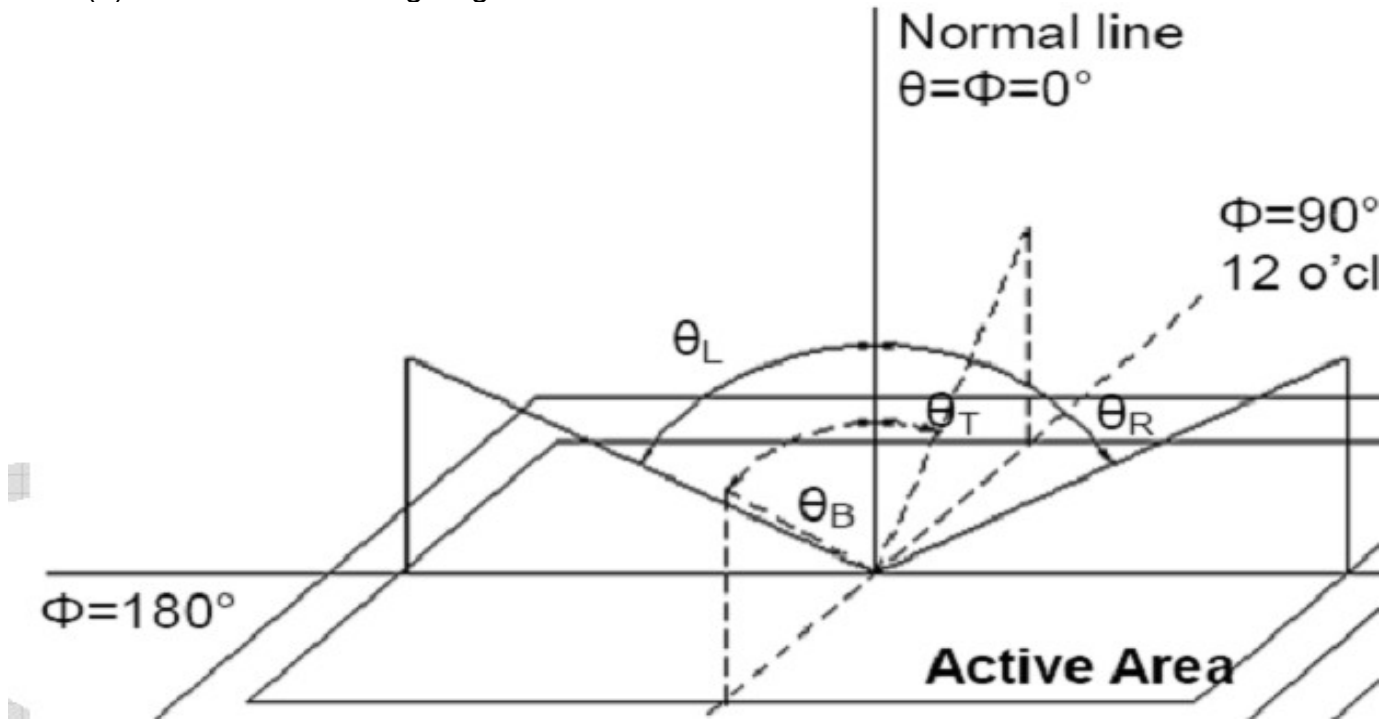
Item	Conditions	Min.	Typ.	Max.	Unit	Note	
Viewing Angle (CR>10)	Horizontal	$\theta_L$	70	80	-	degree	(1),(2),(6)
		$\theta_R$	70	80	-		
	Vertical	$\theta_T$	70	80	-		
		$\theta_B$	70	80	-		
Center Luminance of White	Lc	180	220	-	cd/m <sup>2</sup>		
Contrast Ratio	Center	700	1000	-	-	(1),(3),(6)	
Response Time	Rising	-	25	35	ms	(1),(4),(6)	
	Falling						
CF Color Chromaticity (CIE1931)	Red x	Typ. -0.05	TBD	Typ. +0.05	-	(1), (6)	
	Red y		TBD		-		
	Green x		TBD		-		
	Green y		TBD		-		
	Blue x		TBD		-		
	Blue y		TBD		-		
	White x		TBD		-		
	White y		TBD		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.





Note (2) Definition of Viewing Angle



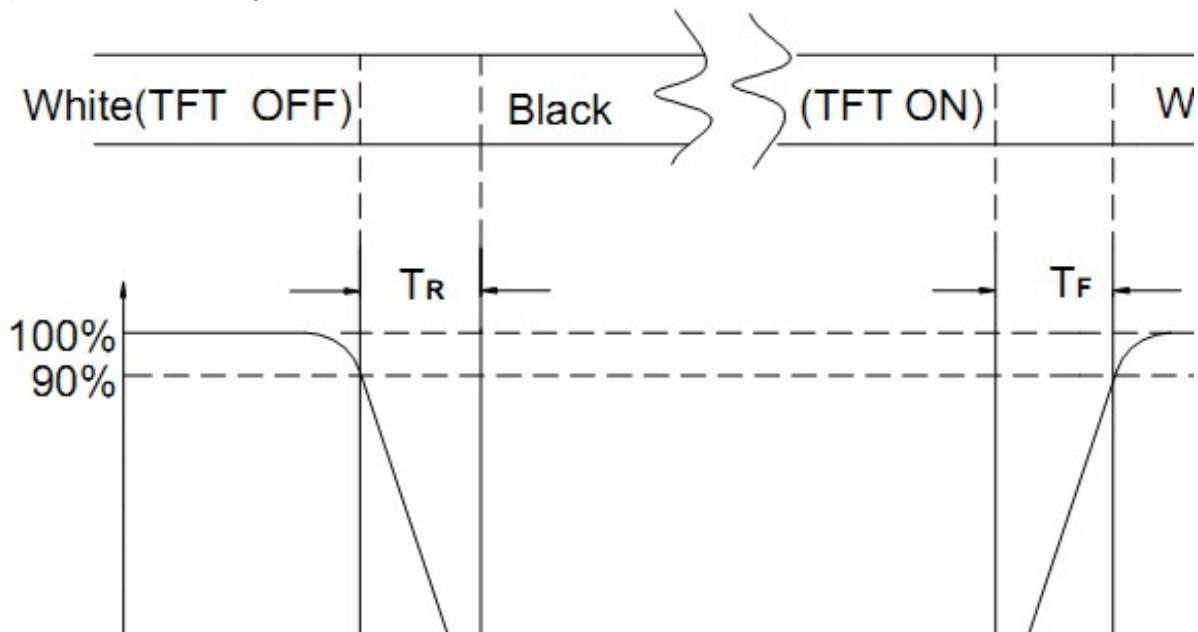
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

$$\text{Transmittance} = \text{Center Luminance of LCD} / \text{Center Luminance of Back Light} \times 100\%$$

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

## 10. Reliability Test Conditions and Methods

No	Item	Condition	Quantity	Criteria
1	High Operating Temperature	Ts = +70°C, 96Hrs	2	IEC60068-2-2 GB2423.2-89
2	Low Operating Temperature	Ta = -20°C, 96Hrs	2	IEC60068-2-1 GB2423.1-89
3	High Humidity	Ta = 60°C, 90%RH, 96Hrs	2	IEC60068-2-3 GB/T2423.3-2006
4	High Storage Temperature	Ta =80°C, 96Hrs	2	IEC60068-2-2 GB2423.2-89
5	Low Temperature Storage	Ta = -30°C, 96Hrs	2	IEC60068-2-1 GB2423.1-89
6	Thermal Cycling Test (non-operation)	-20°C, 60min~70°C, 60min, 20 cycles.	2	IEC60068-2-14 GB2423.22-87
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.14 -2009
8	Electrical Discharge Static	Air:±8KV 150pF/330Ω 5 times	2	IEC61000-4-2 GB/T2423.5-1995
		Contact:±4KV 150pF/330Ω 5 times		
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note1. After the reliability test, the product only guarantee function normally without any fatal defect (non-display, line defect, abnormal display). All the appearance inspection is judged before the reliability test (the product must have enough recovery time, at least 2 hours at room temperature);

Note2. Total current Consumption should be below double of initial value;

Note3. One product only can use to conduct one Item test;

Note4. Ts is the temperature of panel's surface. Ta is the ambient temperature of samples.

## 11. Quality Assurance

### 11.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

### 11.2 Standard for Quality Test

#### 11.2.1 Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

#### 11.2.2 Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

#### 11.2.3 Reliability Test:

Detailed requirement refer to Reliability Test Specification.

### 11.3 Nonconforming Analysis & Disposition

#### 11.3.1 Nonconforming analysis:

11.3.1.1 Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3 If cannot finish the analysis on time, customer will be notified with the progress status.

#### 11.3.2 Disposition of nonconforming:

11.3.2.1 Non-conforming product over PPM level will be replaced.

11.3.2.2 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

### 11.4 Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1 There is any discrepancy in standard of quality assurance.

11.4.2 Additional requirement to be added in product specification.

11.4.3 Any other special problem.

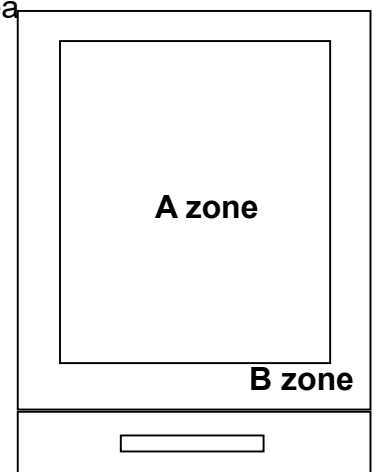
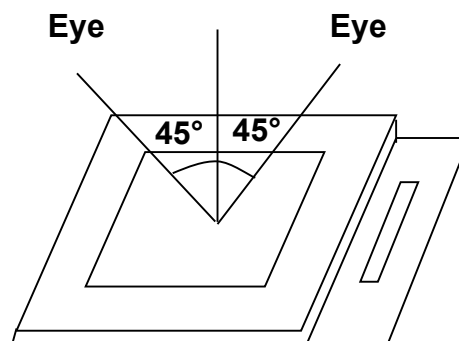
### 11.5 Standard of the Product Visual Inspection

#### 11.5.1 Appearance inspection:

11.5.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area

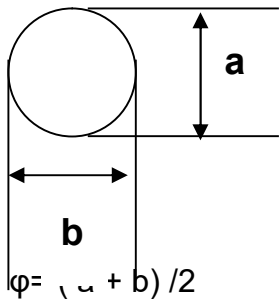
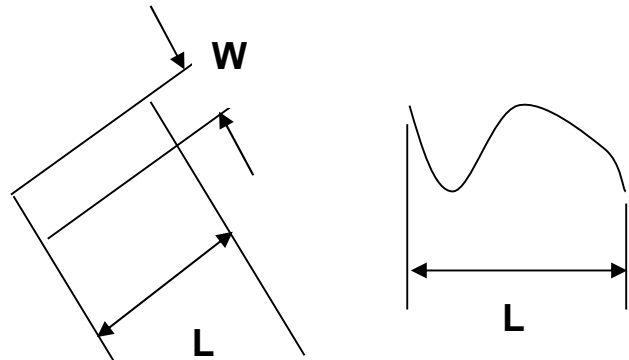


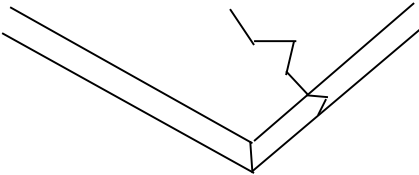
11.5.2 Basic principle:

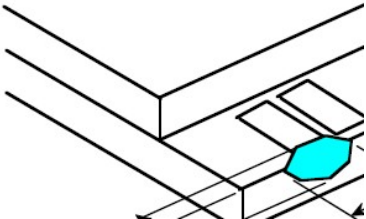
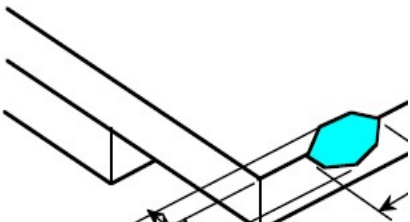
11.5.2.1 A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

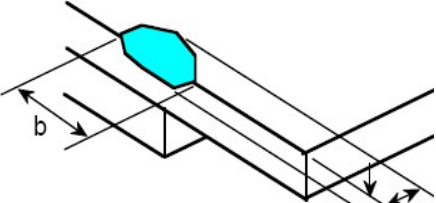
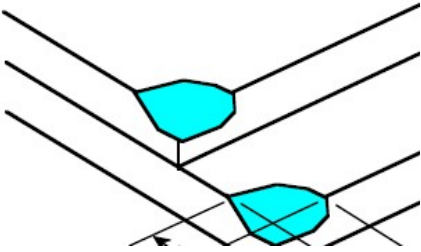
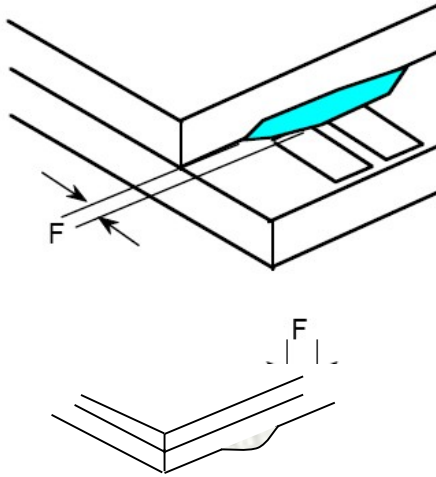
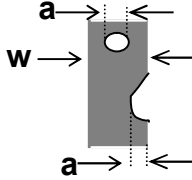
11.5.2.2 New item must be added on time when it is necessary.

11.6 Inspection Specification

No.	Item	Criteria (Unit: mm)																		
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 <table border="1" data-bbox="901 504 1412 705"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.20</math></td> <td></td> <td>Ignore</td> </tr> <tr> <td><math>0.20 &lt; \phi \leq 0.40</math></td> <td></td> <td><math>N \leq 3</math></td> </tr> <tr> <td><math>0.40 &lt; \phi</math></td> <td></td> <td>0</td> </tr> </tbody> </table> <p><math>\phi = (a + b) / 2</math></p> <p>Distance between 2 defects should more than 5mm apart.</p>	Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.40$		$N \leq 3$	$0.40 < \phi$		0						
Size	Area	Acc. Qty																		
$\phi \leq 0.20$		Ignore																		
$0.20 < \phi \leq 0.40$		$N \leq 3$																		
$0.40 < \phi$		0																		
02	Electrical Defect (Minor defect)	<table border="1" data-bbox="542 884 1412 1120"> <thead> <tr> <th>Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td></td> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> </tr> <tr> <td>Dark dot</td> <td><math>N \leq 3</math></td> <td><math>N \leq 3</math></td> </tr> <tr> <td>Total dot</td> <td><math>N \leq 4</math></td> <td><math>N \leq 4</math></td> <td></td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td></td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>	Bright dot	Display Area	Total	Note1		$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 3$	$N \leq 3$	Total dot	$N \leq 4$	$N \leq 4$		Mura	Not visible through 5% ND filters.		
Bright dot	Display Area	Total	Note1																	
	$N \leq 2$	$N \leq 2$																		
Dark dot	$N \leq 3$	$N \leq 3$																		
Total dot	$N \leq 4$	$N \leq 4$																		
Mura	Not visible through 5% ND filters.																			
03	Black and White line Scratch Foreign material (Line type) (Minor defect)	 <table border="1" data-bbox="598 1691 1236 1960"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td><math>W \leq 0.1</math></td> <td>Ignore</td> </tr> <tr> <td><math>L \leq 2</math></td> <td><math>0.1 &lt; W \leq 0.2</math></td> <td>3</td> </tr> <tr> <td><math>L &gt; 2</math></td> <td><math>0.2 &lt; W</math></td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2$	$0.1 < W \leq 0.2$	3	$L > 2$	$0.2 < W$	0	Total		3			
Length	Width	Acc. Qty																		
/	$W \leq 0.1$	Ignore																		
$L \leq 2$	$0.1 < W \leq 0.2$	3																		
$L > 2$	$0.2 < W$	0																		
Total		3																		

		acceptable.
04	Glass Crack (Minor defect)	 <p>Crack is potential to enlarge, any type is not allowed.</p>

05	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>3</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$			
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	3											
$a < \text{Glass Thickness}$												
06	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												

<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &lt; 3.0, b &lt; 3.0</math></td> <td>Ignore</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
<p>09</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>F &lt; 1.0</math></td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											
<p>10</p>	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width <math>a &lt; w/3</math>. (w: circuitry width.)</p> <p>10.2 Open circuit is unacceptable.</p> <p>10.3 No oxidation, contamination and distortion.</p>										

11	Bubble on Polarizer (Minor defect)	Diameter	Acc. Qty
		$\varphi \leq 0.30$	Ignore
		$0.30 < \varphi \leq 0.50$	$N \leq 2$
		$0.50 < \varphi$	$N = 0$
12	Dent on Polarizer (Minor defect)	Diameter	Acc. Qty
		$\varphi \leq 0.25$	Ignore
		$0.25 < \varphi \leq 0.50$	$N \leq 4$
		$0.50 < \varphi$	None
13	Bezel	13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.	
14	Touch Panel	D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$ , $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$ , $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.	
15	LCD Ripple	Touch the touch panel, cannot see the LCD ripple. Pen: R 0.8mm silicon rubber. Operation Force: 120g	
16	PCB	16.1 No distortion or contamination on PCB terminals. 16.2 All components on PCB must same as documented on the BOM/component layout. 16.3 Follow IPC-A-600F.	
17	Soldering	Follow IPC-A-610C standard	
18	Electrical Defect (Major defect)	The below defects must be rejected. 18.1 Missing vertical / horizontal segment, 18.2 Abnormal Display. 18.3 No function or no display. 18.4 Current exceeds product specifications. 18.5 LCD viewing angle defect.	

		18.6 No Backlight. 18.7 Dark Backlight. 18.8 Touch Panel no function.
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## 12. Handling Precautions

### 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.



- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.  
Usage under the maximum operating temperature, 50%Rh or less is required.

## 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.  
[It is recommended to store them as they have been contained in the inner container at the time of delivery from us

## 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

## 13. Precaution for Use

### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 14. Packing Method

TBD